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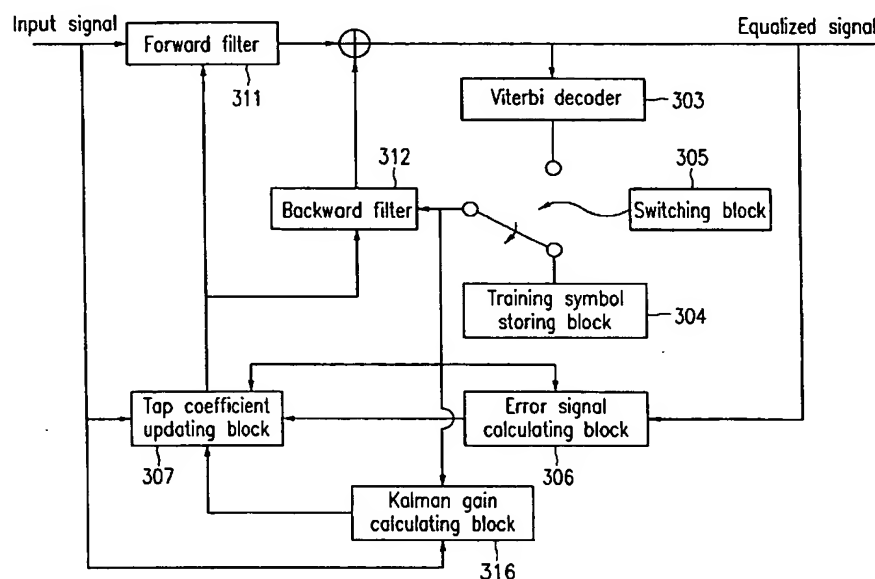
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[Continued on next page]

(54) Title: KALMAN-VITERBI JOINT CHANNEL EQUALIZER



(57) Abstract: In a channel equalizer applicable to a digital television receiver, a forward filter and a backward filter perform filtering to an input signal and a predetermined signal. A Viterbi decoder corrects errors during a transmission procedure in a blind mode. A training symbol storing block stores training symbols. An output signal of the Viterbi decoder and symbols are provided to the backward filter in accordance with a blind mode or a training mode. A Kalman gain is calculated in a Kalman gain calculating block and an error signal is calculated by comparing an equalized signal, symbols, and the output signal of the Viterbi decoder with one another. A tap coefficient is updated by using the calculated error signal and the Kalman gain.

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**KALMAN-VITERBI JOINT CHANNEL EQUALIZER****BACKGROUND OF THE INVENTION****(a) Field of the Invention**

5           The present invention relates to an equalizer and a control method of the equalizer which are used for improving performance in a receiver under a multi-path communication environment of indoor, portable, and mobile reception.

**(b) Description of the Related Art**

10           In the ATSC (Advanced Television Systems Committee) standard which has been adopted as a digital television broadcasting standard in North America, there is a problem in that transmission signals are distorted under an indoor and mobile communication environment due to variable channel and multi-path effects. This reduces reception performance in a  
15   receiver such as a digital television.

          In a digital communication system, data transmission is performed in symbol units through a restricted frequency band width, and pulse energy of a symbol may be dispersed into neighboring symbol pulses due to a time dispersion effect. This induces interference in the neighboring symbols. In  
20   addition, transmitted data is affected by various kinds of channel distortions, caused by such effects as a multi-path effect, a frequency offset effect, a phase jitter effect, and so on. These effects induce ISI (Inter-Symbol Interference) which may affect neighboring symbols of a transmission symbol

in a digital communication system. Therefore, the ISI becomes a barrier to obtaining desired data in a receiver. In addition, most communication channels require an adaptive equalizer which may update tap coefficients adaptively with respect to time variation, since the above described  
5 distortions are variable.

FIG. 1 illustrates a block diagram of a conventional VSB (vestigial sideband) signal receiving system. In FIG. 1, a tuner 100 receives an RF signal from an antenna and synchronizes the RF signal with a local oscillation signal to transform the RF signal into an IF signal; an NTSC  
10 removing filter 200 removes an NTSC component from the IF signal to prevent degradation of an HDTV due to the NTSC component; an equalizer 300 removes multi-path distortion generated in a transmission channel by equalizing an input signal; a phase tracer 400 removes phase noise from the equalized signal; a trellis decoder 500 performs trellis decoding with respect  
15 to an output signal of the phase tracer 400, and outputs the decoded signal; a data de-interleaver 600 performs reverse interleaving to the input signal; an RS decoder 700 performs Reed-Solomon decoding to the input signal so as to generate error-corrected bit streams; and a de-randomizer 800 provides the decoded signal into other elements of the receiving system.

20 In FIG. 2, a detailed configuration of an ATSC VSB equalizer is illustrated. The equalizer 300 basically uses an LMS algorithm. In FIG. 2, a front filter 301 and a rear filter 302 remove an inter-symbol interference component, which generates distortion, from a received signal. In case of a

blind mode, a quantization block 303 compares the signal outputted from the filters 301 and 302 with a predetermined threshold value and outputs hard-decision data. A training symbol storing block 304 stores a training data symbol which is perceived in a transmitting system. The training symbol is  
5 fetched into the rear filter 302 and an error signal calculating block 306 in a training mode, and it is used for calculating errors of the equalized signal. In case of a blind mode, the output signal of the quantization block 303 and the equalized signal are used for calculating errors. A switching block 305  
10 passes the equalized signal or training symbol therethrough in accordance with the two modes. The calculated error is outputted to a tap coefficient updating block 307.

The tap coefficient updating block 307 receives an error signal and input signal of the filter 301, and updates the tap coefficient by using an LMS algorithm. Such an updating method is as shown in the following Equation 1.

15 [Equation 1]

$$C(n+1)=C(n)+\mu x(n)e(n)$$

In Equation 1,  $c(n)$  is a coefficient of a filter,  $x(n)$  is a received signal,  $e(n)$  is an error signal calculated in the error signal calculating block 306, and  $\mu$  is a step size.

20 As such a channel equalizer, a DFE (decision feedback equalizer) is widely used in a digital television receiver. When a decision error is not generated in a quantization block of the DFE, noise amplification at output of the equalizer, which becomes a problem in a conventional linear equalizer,

does not appear when a rear filter removes ISI due to a symbol determined previously. For this reason, the DFE is widely used in a receiving system. Therefore, it is important that a decision error is not generated at output of a quantization block, so the DFE can be used effectively. For attaining this technical point, a method for reducing decision error by inserting a training symbol at a constant period has been used in a digital communication system, while willingly accepting a reduction in data transmission efficiency. However, not enough training symbols are inserted in most cases. The conventional DFE does not have a rapid enough convergent speed to compensate for distortion of a received signal that is generated due to inter-channel interference under a highly distorted communication environment such as indoor reception, mobile reception, or in a communication environment producing ghost images such as a broadcasting channel. Therefore, a better equalizer that has a rapid convergent speed even in a highly distorted communication environment and is efficient at dealing with distortion even in a data duration without the training symbol being inserted is required.

### **SUMMARY OF THE INVENTION**

In order to solve the above described problem, the present invention provides a fast Kalman-Viterbi joint channel equalizer instead of a conventional equalizer using an LMS algorithm.

An equalizer according to one aspect of the present invention comprises:

a forward filter and a backward filter for receiving an input signal and a predetermined signal and performing filtering to the signals;

a Viterbi decoder for correcting errors during a transmission procedure in a blind mode;

5 a training symbol storing block for storing training symbols;

a switching block for classifying an output signal of the Viterbi decoder and symbols in accordance with a training mode and a blind mode, and inputting the output signal of the Viterbi decoder and symbols into the backward filter;

10 a Kalman gain calculating block for receiving input signals of the forward filter and the backward filter and calculating a Kalman gain;

an error signal calculating block for calculating an error signal by comparing an equalized signal, symbols, and the output signal of the Viterbi decoder with one another; and

15 a tap coefficient updating block for updating a tap coefficient of the filters by using the calculated error signal and the Kalman gain.

The equalizer of the present invention uses a fast Kalman algorithm, which is a kind of a fast adaptive equalizing algorithm, so as to solve a problem of the slow convergent speed of a conventional LMS equalizer. In  
20 order to minimize a feedback decision error in a blind period, the equalizer of the present invention performs a soft-decision by using a Viterbi algorithm instead of performing a hard-decision of the output signal in a forward filter or a backward filter. An algorithm on a Kalman equalizer is introduced in an

article in IBM journal Re. Dev., vol 18, pp.267-273, May, 1980, entitled  
"Channel Equalization Using a Kalman Filter for Fast Data Transmission" by  
D.N. Godard. A fast Kalman algorithm is introduced in articles in IEEE Trans.  
Comm., vol. COM-26, pp.1439-1446, October, entitled "Application of Fast  
5 Kalman Estimation to Adaptive Equalization" by D.D. Falconer et al. and in  
an IEEE Trans. Acoust., Speech, Signal Processing, vol. ASSP-31, pp1394-  
1402, entitled "A Fast Sequential Algorithm for Least-Squares Filtering and  
Prediction" by G. Carayannis et al. A Viterbi algorithm is introduced in an  
article in IEEE Trans. Inform. Theory, vol. IT-13, pp. 260-269, April, entitled  
10 "Error Bounds for Convolutional Codes and an Asymptotically Optimum  
Decoding Algorithm" by A.J. Viterbi.

The equalizer of the present invention operates in two kinds of  
modes, a training mode and a blind mode. In the training mode, a  
transmitting part transmits a training symbol, which is previously perceived by  
15 a receiving part, during a predetermined period for channel equalization.  
The equalizer compares a signal distorted through transmission of a channel  
with the above perceived training symbol to determine characteristics of the  
channel. In a blind mode, when a training symbol does not exist after the  
training mode, the equalizer performs equalization by using only probability  
20 distribution of a signal symbol in a transmitting part and a received data  
signal. Therefore, it is not easy to solve distortion due to interference of  
inter-symbols in a blind mode compared to a training mode. The equalizer of  
the present invention is illustrated in FIG. 3. The equalizer has



characteristics in that it comprises a forward filter and a backward filter for receiving an input signal and a predetermined signal, and it performs filtering to the signals; a Viterbi decoder for correcting errors during a transmission procedure in a blind mode; a training symbol storing block for storing training symbols; a switching block for classifying an output signal of the Viterbi decoder and symbols in accordance with a training mode and a blind mode and inputting the output signal of the Viterbi decoder and symbols into the backward filter; a Kalman gain calculating block for receiving input signals of the forward filter and the backward filter and calculating a Kalman gain; an error signal calculating block for calculating an error signal by comparing an equalized signal, symbols, and the output signal of the Viterbi decoder with one another; and a tap coefficient updating block for updating a tap coefficient of the filters by using the calculated error signal and the Kalman gain. The present invention also has characteristics in that the equalizer according to the present invention updates a coefficient of filters by using a fast Kalman algorithm while a conventional equalizer updates the coefficient of filters by using an LMS algorithm. Further, the equalizer according to the present invention removes transmission error in a blind mode by using a Viterbi decoder instead of a quantization block.

20

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

The accompanying drawings, which are incorporated in and constitute a part of the specification, illustrate an embodiment of the invention, and together with the description, serve to explain the principles of

the invention:

FIG. 1 illustrates a schematic block diagram of a conventional VSB signal receiving system.

FIG. 2 illustrates a structure of an ATSC standard equalizer.

5        FIG. 3 illustrates a block diagram of a Kalman-Viterbi joint channel equalizer according to an embodiment of the present invention.

FIG. 4 illustrates a procedure for calculating Euclidian distance of a Viterbi decoding algorithm and searching a life path.

#### **DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS**

10        In the following detailed description, only the preferred embodiment of the invention has been shown and described, simply by way of illustration of the best mode contemplated by the inventor(s) of carrying out the invention. As will be realized, the invention is capable of modification in various obvious respects, all without departing from the invention.  
15        Accordingly, the drawings and description are to be regarded as illustrative in nature, and not restrictive.

At first, a fast Kalman algorithm will be described by referring to Falconer's paper in the following.

Various kinds of signals, filter coefficients, and other variables may  
20        be defined as below.

$x_N(n)$  : input signal vector for overall filters

$y(n)$  : forward filter input signal sample

$d(n)$  : desired signal

$$x_N(n) = [y(n-1), \dots, y(n-N_1) | d(n) \dots d(n-N_2)]^T$$

$$\xi_p(n) = [y(n) d(n)]^T$$

$$\rho_p(n) = [y(n-N_1) d(n-N_2)]^T$$

5  $\varepsilon_p(n)$  : forward preceding error

$\varepsilon_p(n)'$  : forward following error

$A_{Np}(n)$  : forward prediction coefficient matrix

$E_{pp}(n)$  : forward sum error

$\eta_p(n)$  : backward prediction error

10  $k_N(n)$  : Kalman gain

$D_{Np}(n)$  : backward prediction coefficient matrix

$C_N(n)$  : tap coefficient for overall filters

When computation starts, the forward preceding error, forward following error, forward prediction coefficient matrix, and forward sum error  
15 are evaluated by using the following Equation 2.

[Equation 2]

$$\begin{aligned}
\varepsilon_p(n) &= \xi_p(n) + A_{Np}(n-1)^T x_N(n) \\
A_{Np}(n) &= A_{Np}(n-1) - k_N(n) \varepsilon_p(n)^T \\
\varepsilon_p(n)' &= \xi_p(n) + A_{Np}(n)^T x_N(n) \\
E_{pp}(n) &= \lambda E_{pp}(n-1) + \varepsilon_p(n)' \varepsilon_p(n)^T
\end{aligned}$$

When we obtain the forward preceding error, forward following error, forward prediction coefficient matrix, and forward sum error, the Kalman gain may be evaluated through the following Equation 3 by using the above results.

[Equation 3]

$$\begin{aligned}
\bar{k}_M(n) &= S_{MM} \left[ \frac{E_{pp}(n)^{-1} \varepsilon_p(n)'}{k_N(n) + A_{Np}(n) E_{pp}(n)^{-1} \varepsilon_p(n)'} \right] \\
Q_{MM} \bar{k}_M(n) &= \left( \frac{m_N(n)}{\mu_p(n)} \right) \\
\eta_p(n) &= \rho_p(n) + D_{Np}(n-1)^T x_N(n+1) \\
D_{Np}(n) &= [D_{Np}(n-1) - m_N(n) \eta_p(n)^T] [I_{pp} - \mu_p(n) \eta_p(n)^T]^{-1} \\
k_N(n+1) &= m_N(n) - D_{Np}(n) \mu_p(n)
\end{aligned}$$

The  $S_{MM}$  and  $Q_{MM}$  in equation 3 have operations as below.

$$\text{When } x_N(n) = \begin{bmatrix} y(n-1) \\ y(n-2) \\ \vdots \\ y(n-N_1) \\ \hline d(n-1) \\ d(n-2) \\ \vdots \\ d(n-N_2) \end{bmatrix} \text{ and } \bar{x}_M(n) = \begin{bmatrix} y(n) \\ \hline y(n-1) \\ \vdots \\ y(n-N_1) \\ \hline d(n) \\ \hline d(n-1) \\ \vdots \\ d(n-N_2) \end{bmatrix} \text{ are given}$$

above, the following relationship may come into existence.

$$S_{MM} \bar{x}_M(n) = \begin{bmatrix} \xi_p(n) \\ \hline x_N(n) \end{bmatrix}, \quad Q_{MM} \bar{x}_M(n) = \begin{bmatrix} x_N(n+1) \\ \hline \rho_p(n) \end{bmatrix}$$

A Viterbi decoder is a kind of a maximum likelihood decoder that is  
 5 used for decoding bit streams having coded information in a communication  
 system. When an actually transmitted value provided from the exterior of the  
 decoder is estimated on the basis of a state metric which is an observation  
 value passing through a communication channel, the Viterbi decode  
 determines a maximum likelihood state metric value, which makes a relevant  
 10 probability maximum with respect to an actually transmitted value and an  
 observation value, as an estimation value with respect to the actually  
 transmitted value. The Viterbi decoder stores a life path into a path memory,  
 and then decodes a symbol by tracing back the path memory on the basis of  
 the state metric. When symbols to be decoded are sequentially inputted into

the decoder and at the same time all of the life paths are inputted in the initial stage, the Viterbi decoder traces back the path memory in units of one time tracing, and decodes the following symbol.

The present invention has been conceived to improve the convergent speed and reduce residual error by combining a fast Kalman algorithm into the Viterbi decoder. As one embodiment of such an application, the present invention provides a method for improving equalization performance in an 8-VSB system, which is a North American type ground wave digital television standard.

A Kalman-Viterbi joint channel equalizer according to the present invention is illustrated in FIG. 3.

As shown in FIG. 3, the Kalman-Viterbi joint channel equalizer comprises a forward filter 311, a backward filter 312, a Viterbi decoder 313, a training symbol storing block 314, a switching block 315, a Kalman gain calculating block 316, an error signal calculating block 317, and a tap coefficient updating block 318.

Output signals of the filters 311 and 312 may be represented as in the following Equation 4.

[Equation 4]

$$y[n] = \sum_{i=0}^{N_b-1} b_i[n]x[n-i] + \sum_{j=1}^{N_a} a_j[n]\hat{d}[n-j]$$

In the above Equation 4,  $x[n]$  is an input signal of the filters 311 and 312 at time  $n$ ,  $y[n]$  is an output signal of the filters 311 and 312 at time  $n$ ,

$b_i[n]$  is a tap coefficient of the forward filter 311 at time  $n$ ,  $a_i[n]$  is a tap coefficient of the backward filter 312 at time  $n$ ,  $N_b$  is a tap number of the forward filter 311,  $N_a$  is a tap number of the backward filter 312, and  $\hat{d}[n]$  is a training symbol in case of a training mode while being an output signal of the Viterbi decoder in case of a blind mode.

As can be seen from the above Equation 4, the forward filter 311 sequentially delays digital input data  $x[n]$  sampled at a bit rate of 10.76 MHz, multiplies the digital input data  $x[n]$  by  $b_i[n]$  sequentially, and adds each result of the multiplication to output equalized data. Then, the backward filter 312 has the same operation as the forward filter 311, excepting that input of the backward filter 312 is a training symbol or an output signal of the Viterbi decoder. While performing these operations periodically, the filters 311 and 312 adjust tap coefficients to optimal values on the basis of the information inputted from the tap coefficient updating block 318, and perform filtering to the input signal, thereby obtaining desired output data. At this time, the forward filter 311 performs filtering to symbols inputted from the exterior of the decoder, and removes interference between the exterior signals. The backward filter 312 performs filtering by using a previously detected value so as to remove an interference signal of the present symbol, which is induced by the previously detected symbol.

The switching block 315 determines a training mode or a blind mode. In case of the training mode, the switching block 315 outputs training symbols from the training symbol storing block 314. In case of the blind

mode, the switching block 315 uses the output of the Viterbi decoder 313 instead of a simple quantizer used as a symbol detector in a conventional equalizer. The Viterbi decoder 313 starts to operate in the blind mode right after the training mode. The Viterbi decoder 313 receives output of the filters 311 and 312, and corrects a decision error of a transmission signal by performing a soft-decision. Therefore, the convergent speed in the blind mode may be improved. At this time, the Viterbi decoder 313 performs decoding by using a method for selecting a minimum distance among Euclidian distances.

FIG. 4 illustrates an example of the Viterbi decoding algorithm. As shown in Equation 3, the computation amount increases in a fast Kalman algorithm when the number of additionally inputted data and outputted data increase. Therefore, it is required that the tracing back length is minimized in the Viterbi decoder in order to reduce the computation amount. When a fast Kalman algorithm is applied to a DFE, the multiplication computation amount may be represented as in the following Equation 5.

[Equation 5]

$$\text{Computation Amount} : 7Np + Np^2 + 3p^2 + 2N + (4/3)p^3 - p/3$$

$$p = 2 * (1 + \text{trace\_back\_len} * 12)$$

N: number of equalizer taps

p: number of additional data inputted from a fast Kalman algorithm

trace\_back\_len: tracing back length at Viterbi decoder

Accordingly, the shorter the tracing back length becomes, the less



the multiplication computation amount of the algorithm used in the blind mode becomes. It is possible to obtain a more accurate value by increasing the tracing back length. When the computation amount decreases or the decoding delay value is long as in a digital broadcasting system using twelve TCM coders, the tracing back length is set to "1" to decrease the delay value, and hence the multiplication computation amount of the equalizer used in the blind mode may decrease. Through application of the above method, hardware complexity may be reduced by 1/state as compared with a PSP (per-survivor processing) algorithm which is a conventional equalizing method. In addition, a problem that the PSP algorithm does not remove pre-echo may be solved.

The error signal calculating block 317 receives an equalized signal and training symbol or output of the Viterbi decoder, and then calculates an error signal through the following Equation 6.

[Equation 6]

$$e(n+1) = d(n+1) - C_N(n)^T x_N(n+1)$$

The Kalman gain calculating block 316 receives input signals of the forward filter 311 and the backward filter 312, and calculates Kalman gain by using the Equations 2 and 3. Then, the Kalman gain calculating block 316 transmits the calculated Kalman gain into the tap coefficient updating block 318, which performs tap coefficient updating by using the above error signal and the Kalman gain, and transmits the tap coefficient into each of the filters 311 and 312. An equation for updating of tap coefficients is shown in the

following Equation 7.

[Equation 7]

$$C_N(n+1) = C_N(n) + k_N(n+1)e(n+1)$$

The tap coefficient updating block 318 transmits a tap coefficient  
5 determined by the above Equation 7 into each of the filter 311 and 312.

When channel equalization has been finished at time index  $n$  through the above described procedure, the time index  $n$  is incremented by "1". The channel equalization is performed by repeating the above procedure.

10 According to the present invention, the fast Kalman-Viterbi joint channel equalizer may improve reception performance of an N level VSB digital television under a dynamic multi-path environment.

While this invention has been described in connection with what is presently considered to be the most practical and preferred embodiment, it is  
15 to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims. For example, all the possible equalizing algorithms such as a traditional Kalman and RLS algorithm, a fast RLS (recursive least  
20 squares), or an LMS system may be applied to the equalizer of the present invention. Therefore, it will be obvious for those skilled in the art to change, alter, or adjust the disclosed embodiments without departing from the spirit and scope of the invention. Although the present invention has been

described and illustrated in detail, it is clearly understood that the same is for illustration and example only, and is not to be taken as limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.

**WHAT IS CLAIMED IS:**

1. A Kalman-Viterbi joint channel equalizer, comprising:

a forward filter and a backward filter for receiving an input signal and a predetermined signal and performing filtering to the signals;

5 a Viterbi decoder for correcting errors during a transmission procedure in a blind mode;

a training symbol storing block for storing training symbols;

a switching block for classifying an output signal of the Viterbi decoder and symbols in accordance with a training mode and a blind mode,  
10 and inputting the output signal of the Viterbi decoder and symbols into the backward filter;

a Kalman gain calculating block for receiving input signals of the forward filter and the backward filter and calculating a Kalman gain;

an error signal calculating block for calculating an error signal by  
15 comparing an equalized signal, symbols, and the output signal of the Viterbi decoder with one another; and

a tap coefficient updating block for updating a tap coefficient of the filters by using the calculated error signal and the Kalman gain.

2. The Kalman-Viterbi joint channel equalizer according to claim 1,  
20 wherein the Kalman gain calculating block updates the tap coefficient by applying a fast Kalman algorithm in a blind mode.

3. The Kalman-Viterbi joint channel equalizer according to claim 2, wherein the Viterbi decoder uses a Viterbi decoding algorithm in a blind

mode.

4. The Kalman-Viterbi joint channel equalizer according to any one of claims 1 to 3, wherein the Viterbi decoder reduces a tracing back length to use the reduced tracing back length in obtaining errors for tap coefficient updating.

5. The Kalman-Viterbi joint channel equalizer according to any one of claims 1 to 3, wherein the Viterbi decoder corrects errors by skipping symbols in a predetermined number of symbols to obtain errors for tap coefficient updating.

6. The Kalman-Viterbi joint channel equalizer according to any one of claims 1 to 3, wherein a symbol location of a robust stream is previously recognized and is applied to a data duration blind algorithm using the Viterbi decoder when an E-VSB is applied to the Kalman-Viterbi joint channel equalizer.

7. A VSB signal receiving system, comprising:

a tuner for receiving an RF signal from an antenna and synchronizing the RF signal with a local oscillation signal to transform the RF signal into an IF signal;

an NTSC removing filter for removing an NTSC component from the IF signal to prevent degradation of an HDTV due to the NTSC component;

an equalizer for removing multi-path distortion generated in a transmission channel by equalizing the input signal;

a phase tracer for removing phase noise from the equalized signal;

a trellis decoder for performing trellis decoding with respect to an output signal of the phase tracer and outputting a decoded signal;

a data de-interleaver for performing reverse interleaving to the decoded signal;

5           an RS decoder for performing Reed-Solomon decoding to the output signal of the data de-interleaver so as to generate error-corrected bit streams; and

a de-randomizer for providing the output signal of the RS decoder to other elements of the VSB signal receiving system,

10           wherein the equalizer further comprises:

a forward filter and a backward filter for receiving an input signal and a predetermined signal and performing filtering to the signals;

a Viterbi decoder for correcting errors during a transmission procedure in a blind mode;

15           a training symbol storing block for storing training symbols;

a switching block for classifying the output signal of the Viterbi decoder and symbols in accordance with a training mode and a blind mode and inputting the output signal of the Viterbi decoder and symbols into the backward filter;

20           a Kalman gain calculating block for receiving input signals of the forward filter and the backward filter and calculating a Kalman gain;

an error signal calculating block for calculating an error signal by comparing an equalized signal, symbols, and the output signal of the Viterbi

decoder with one another; and

a tap coefficient updating block for updating a tap coefficient of the filters by using the calculated error signal and the Kalman gain.

1/3  
FIG.1

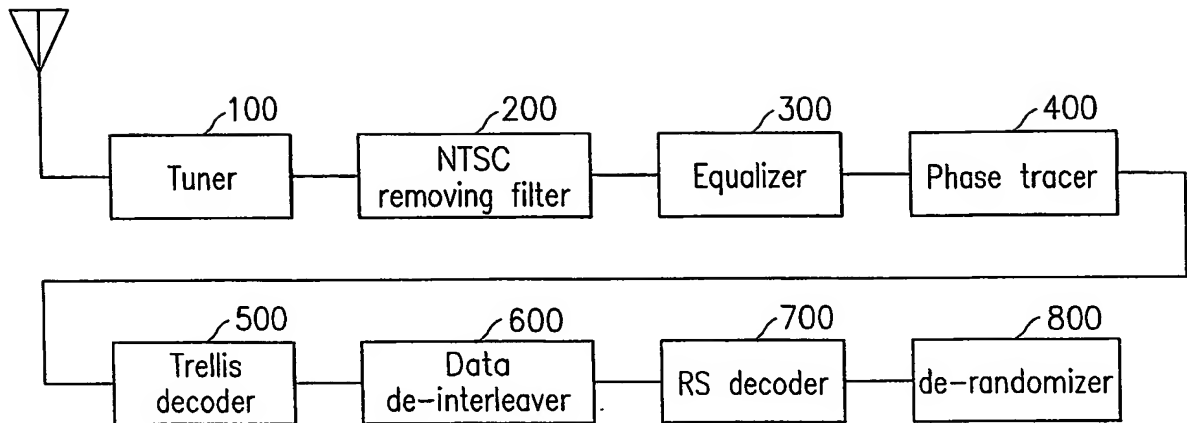
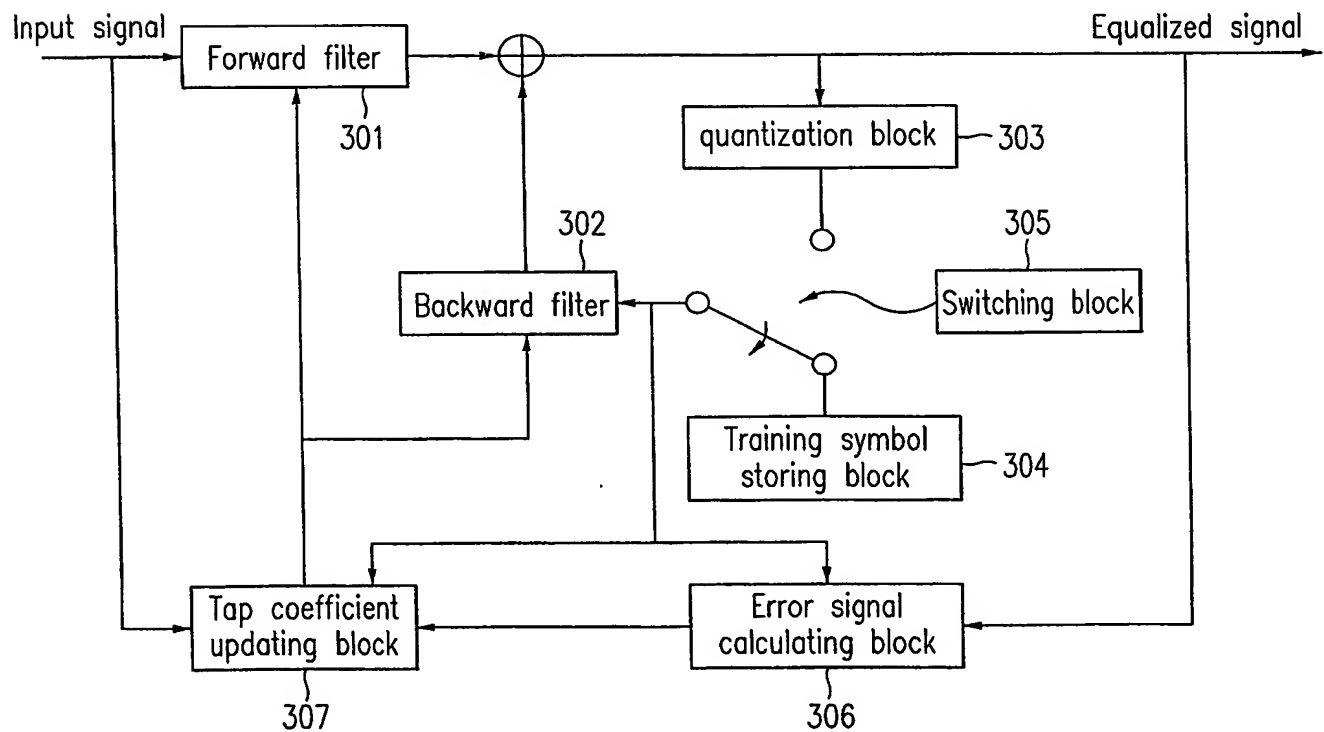
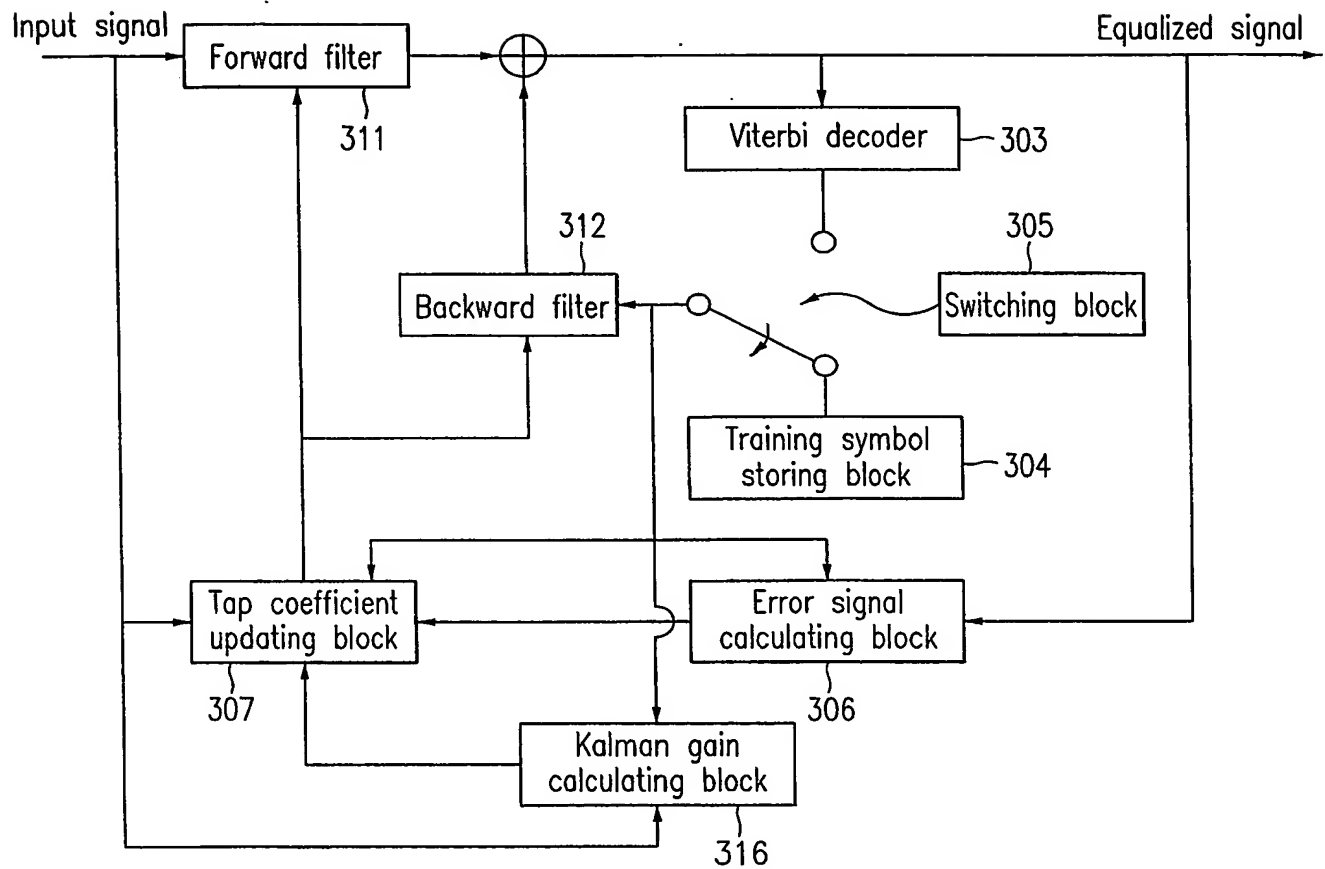


FIG.2





2/3  
FIG.3

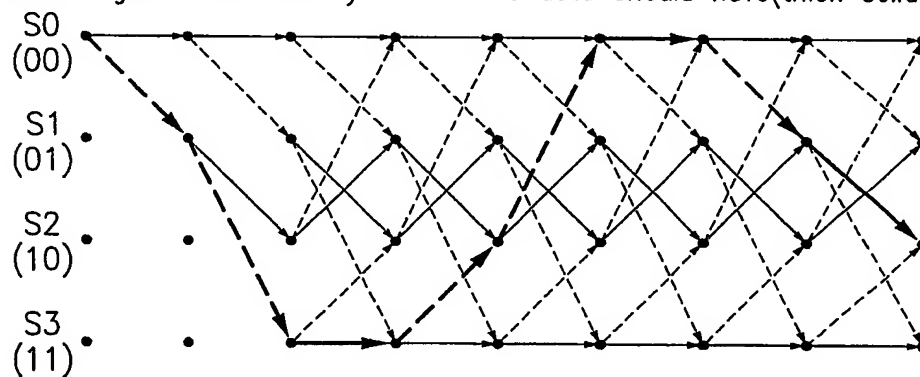
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FIG.4

Input data and output value when the following input data are given

input data $Y_1$	1	1	0	1	1	0	1	0	1
input data $Y_2$	1	1	0	0	1	1	0	0	0
transmit data	110	111	001	011	110	100	010	001	010
transmit value	5	7	-5	-1	5	1	-3	-5	-3
received value	5	6.5	-5	-1.5	5	1	-3	-5	-3

Trellis diagram that actually transmitted data should have(thick solid line)



# INTERNATIONAL SEARCH REPORT

International application No.  
PCT/KR03/00779

## A. CLASSIFICATION OF SUBJECT MATTER

IPC7 H04N 7/015

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int. Cl.6 H04N 5/14, Int. Cl. 7 H04B 7/005, H03M 13/41, H04B 1/10, 3/06, 7/26

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
KR, JP. IPC as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
eKIPASS "KALMAN-VITERBI", "JOINT", "CHANNEL", "EQUALIZER"

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	KR 1999-0038973 A (HYUNDAE ELECTRONICS INDUSTRIAL CO., LTD), June 05, 1999 (05. 06. 1999), (Family : none), - See the whole document	1-7
A	JP 2001-251226 A (YRPKOKINOIDOTAITSUSHINKENKYUSHO:KK), September 14, 2001 (14. 09. 2001), (Family : none), - See the whole document	1,3,4,5,7

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

\* Special categories of cited documents:

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"&" document member of the same patent family


Date of the actual completion of the international search

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